

SECRET

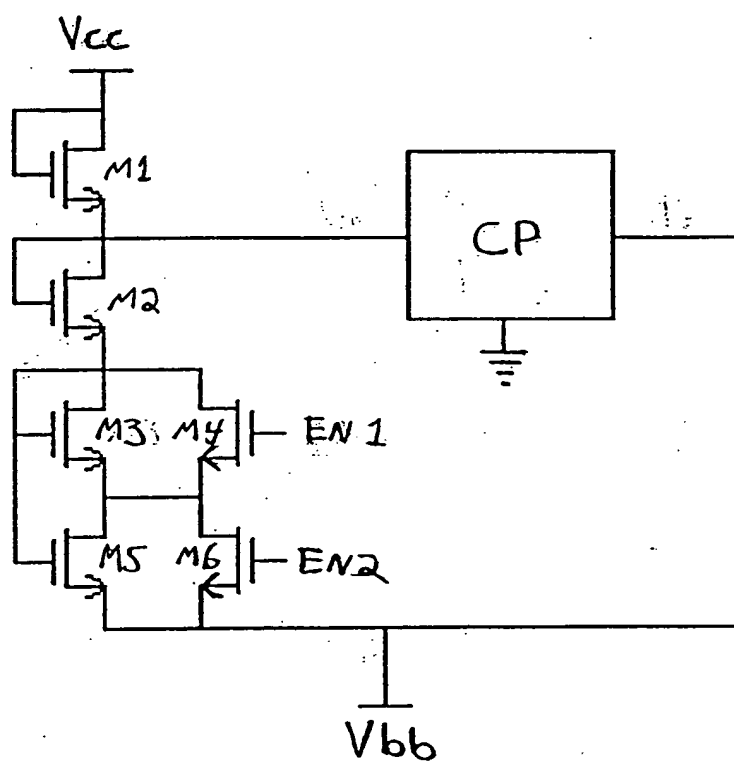


Figure 1.

$$V_{GS} = 1 - V_S$$

$$V_{DS} = -1.0 - V_S$$

$$V_{GS} - V_{DS} = 2$$

$$V_{GS} = V_{DS} + 2$$

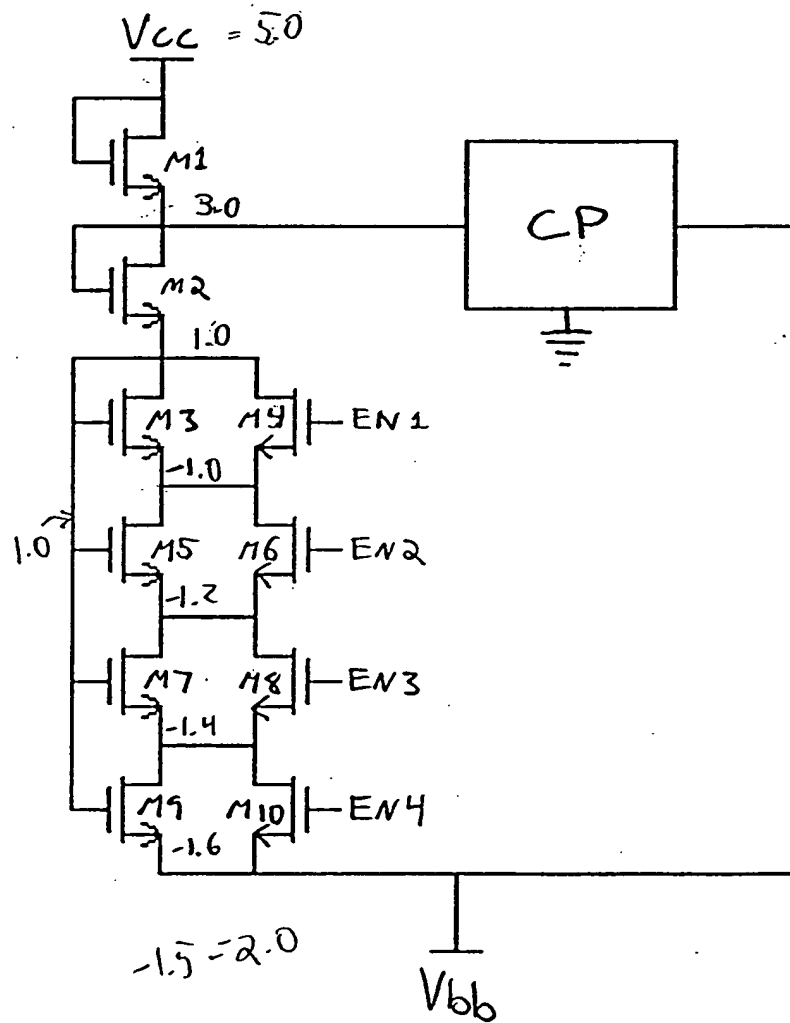


Figure 2

The diagram illustrates a 3T1C1M1 circuit. It features a stack of transistors M1 through M9. M1 and M2 are connected to VCC. M3, M5, M7, and M9 are in a vertical stack. M4 and M6 are connected to the gates of M3 and M5 respectively, with outputs EN1 and EN2. A capacitor CP is connected between the node between M2 and M3 and ground.

Figure 3